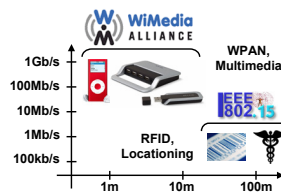
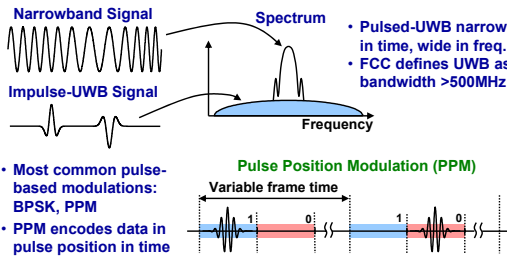


DELAY-BASED BPSK FOR PULSED-UWB COMMUNICATION

David D. Wentzloff and Anantha P. Chandrakasan
Massachusetts Institute of Technology, Cambridge, MA

Background

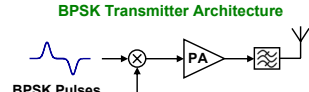
What is Pulsed-UWB Signaling?



- UWB has a wide range of applications
- Optimal architecture depends on data rate
- High data rate – coherent (BPSK, OFDM)
- Low data rate – non-coherent (PPM)

Motivation

- BPSK is the most commonly used modulation – Used as primary modulation, or for scrambling spectrum
- Signal inversion for BPSK costly to implement in hardware (increased power and die area)

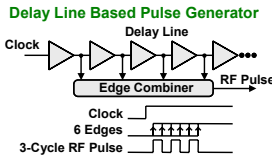


- Eliminating signal inversion enables digital architecture

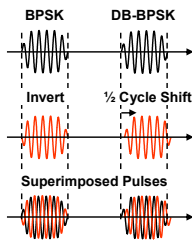
All-digital transmitter → Reduced power and area Benefits from process scaling

Delay-Based BPSK Modulation Technique

- Instead of inverting pulses (BPSK), delay by 1/2 of an RF period
- Suitable for delay line based class of pulse generators
- Synthesize pulses from series of edges
- Delay/stage is equal to 1/2 RF period



Comparison of DB-BPSK to BPSK



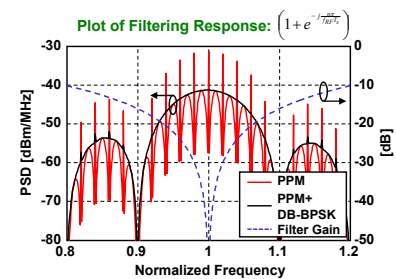
- Signal spectrum can generally be described by continuous and discrete parts
- BPSK has only a continuous part

$$\text{BPSK: } P(f) = \frac{1}{T} |S(f)|^2$$

- DB-BPSK has continuous and discrete parts with additional filter term

$$\text{DB-BPSK: } P(f) = \frac{1}{T} |S(f)|^2 - \frac{1}{4T} |S(f)|^2 \left(1 + e^{-j\pi f T}\right)^2 + \frac{1}{4T} \sum_{n=1}^{\infty} \left| \frac{n}{T} \right| \left(1 + e^{-j\pi f T}\right)^2 \delta\left(f - \frac{n}{T}\right)$$

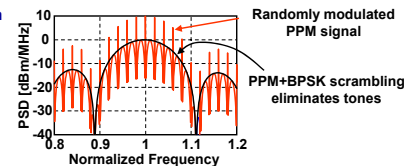
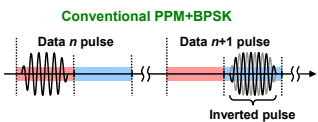
- At $f = f_{RF}$, the filter term = 0 and the equations collapse to only a continuous part



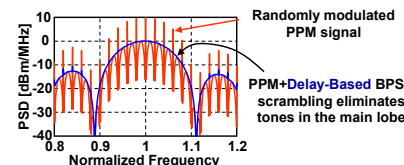
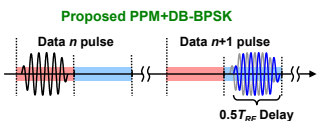
Pulsed-UWB Applications

Scrambling 2-PPM Signals

- PPM is known to produce a line spectrum
- Scrambling used to eliminate tones

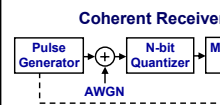


- BPSK is costly to implement in hardware
- DB-BPSK eliminates tones in main lobe

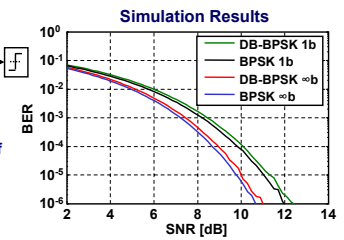


Replacement for BPSK modulation

- DB-BPSK can replace BPSK in a coherent receiver
- Simulated performance with a matched filter receiver architecture



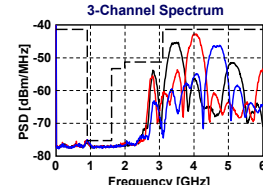
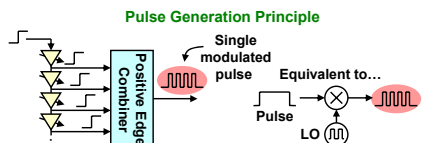
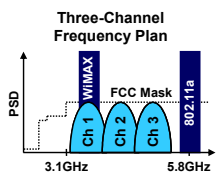
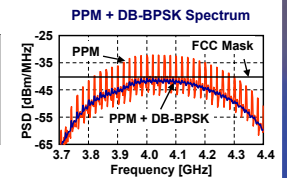
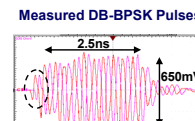
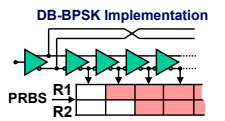
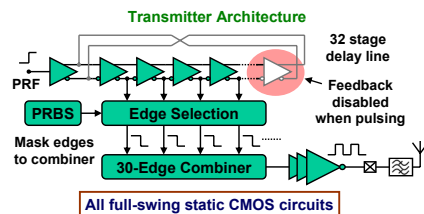
- Performance loss is function of number of cycles/pulse
- Loss is independent of ADC resolution in receiver



DB-BPSK can replace BPSK in a coherent receiver with 0.2dB loss

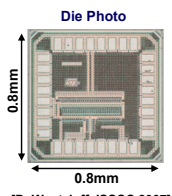
Experimental Verification

- Fabricated delay-line based transmitter RFIC
- All-digital architecture for greatest energy efficiency
- Generates PPM+DB-BPSK pulses in 3.1-5GHz band
- Supports three channels



Results Summary

Technology	90nm CMOS
Active Area	0.2x0.4mm ²
Modulation	PPM
Scrambling	DB-BPSK
Supply	1V
Leakage Power	96μW
Active E/pulse	37pJ/pulse
PRF Range	10kHz to 16.7MHz
Total E/bit	9.6nJ/bit to 43pJ/bit



[D. Wentzloff, ISSCC 2007]